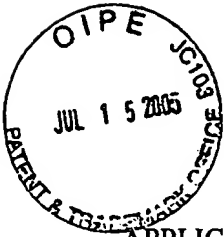


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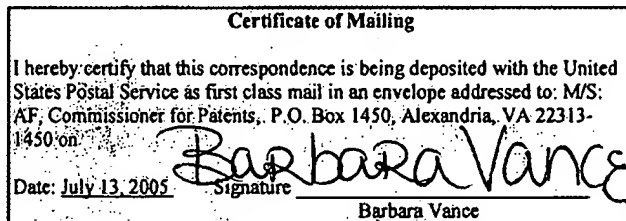
Attorney Docket No.: Intel 2207/10122
Assignee: Intel Corporation
Under 37 C.F.R. §1.116
Expedited Procedure
Examining Group 2183



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Sailesh B. KOTTAPALLI
SERIAL NO. : 09/753,764
FILED : December 29, 2000
FOR : METHOD AND APPARATUS FOR INSTRUCTION POINTER
STORAGE ELEMENT CONFIGURATION IN A
SIMULTANEOUS MULTITHREADED PROCESSOR
GROUP ART UNIT : 2183
EXAMINER : David J. HUISMAN

M/S: AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450



AMENDMENT AND RESPONSE

Dear Sir:

The following amendment and remarks below are respectfully submitted in response to the Final Office Action dated May 13, 2005.

Amendments to the Claims are reflected in the listing of claims which begin on page 2 of this paper.

Amendments to the Drawings begin on page 8 of this paper and include both attached replacement sheets and annotated sheets showing changes.

Remarks/Arguments begin on page 9 of this paper.

An **Appendix** including amended drawings is attached after page 13 of this paper.

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ADG